PATENT HPDNO: 200209699-1

SYSTEM AND METHOD FOR TESTING A COMPONENT IN A COMPUTER SYSTEM USING VOLTAGE MARGINING

Abstract

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A computer system comprising a first processor that is configured to cause an operating system to be booted, a test module, a component coupled to the test module, and a power supply coupled to the test module and the component is provided. The test module is configured to provide a first signal to the power supply to cause a first voltage to be provided to the component, and the test module is configured to cause a first test to be performed on the component subsequent to the first voltage being provided to the component and the operating system being booted.

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